

IN THE CLAIMS:

Please amend claims 11 and 13 as follows:

1-10. (Canceled)

11. (Currently Amended) A liquid crystal display device comprising:  
an active matrix panel having a coloring member;  
a liquid crystal having spontaneous polarization, sealed in said active matrix  
panel; and

a writing/erasing unit for displaying an image on a frame by frame basis by  
repeating a data writing process and a data erasing process for said active matrix panel;  
wherein one frame time comprises a period of the data writing process, a  
period of the data erasing process and a period during which neither the data is held without  
performing either the data writing process nor or the data erasing process is performed.

12. (Previously Presented) The liquid crystal display device as set  
forth in claim 11, further comprising:

a back-light for irradiating white light on said coloring member; and  
a back-light controller for controlling said back-light to be turned on or off  
according to the period of the data writing process, the period of the data erasing process, and

the period during which neither the data writing process nor the data erasing process is performed.

13. (Currently Amended) A liquid crystal display method using a liquid crystal display device constructed by sealing a liquid crystal having spontaneous polarization in an active matrix panel including a coloring member;

wherein an image is displayed on a frame by frame basis by repeating a data writing process and a data erasing process for the active matrix panel; and

one frame time comprises a period of the writing process, a period of the data erasing process and a period during which neither the data is held without performing either the data writing process ~~nor or~~ the data erasing process ~~is performed~~.

14. (Previously Presented) The liquid crystal display method as set forth in claim 13, wherein

white light is irradiated on the coloring member, and irradiation/non-irradiation of the white light is controlled according to the period of the data writing process, the period of the data erasing process and the period during which neither the data writing process nor the data erasing process is performed.